

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	65	scheduler same sram same dram	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 17:19
S2	29	S1 same queue	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 15:01
S3	4	S2 and hierarch\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:25
S4	100	(queue near2 manage\$5) same sram same dram	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 15:01
S5	46	S4 and hierarch\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:46
S6	9	S1 same (enqueue or dequeue)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 15:16
S7	3	CELL/FRAME SCHEDULING METHOD AND COMMUNICATIONS CELL/FRAME SCHEDULER	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 15:14
S8	8081	dram with read only	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:32

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S9	7	S5 and S8	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:31
S10	1	S8 same (enqueue or dequeue)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:33
S11	43	S5 and (@ad<="20050205" or @rlad<="20050205" or @prad<="20050205")	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:49
S12	43	S11 and pipeline	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:47
S13	35	S11 and (enqueue or dequeue)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:50
S14	3762	read-modify-write	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:52
S15	0	S14 and S13	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:51
S16	0	S14 and S11	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:51

EAST Search History

S17	5748	read near3 modify near3 write	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:52
S18	1	S11 and S17	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:53
S19	0	S13 and S17	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 16:52
S20	21	sram same dram same enqueue same dequeue	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:23
S21	2	S20 and S14	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 17:30
S22	3762	S17 and S14	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 17:29
S23	3	S17 and S20	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 17:31
S24	8	S20 and hierarch\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:21

EAST Search History

S25	28	sram same dram same (read near2 modify near2 write)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:24
S26	1	S25 same (queue manage\$3 or schedule\$3 or classif\$3)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:25
S27	7	S25 and (queue manage\$3 or schedule\$3 or classif\$3)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:39
S28	1	S27 and hierarch\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:26
S29	2	S25 and hierarch\$4	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:26
S30	0	Parallel asynchronous propagation pipeline structure and methods to access multiple memory arrays	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/14 18:39
S31	2	"6922732".pn.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/15 14:09
S32	2	"6035372".pn.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/16 07:18

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S33	2	"20060206684"	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/11/16 07:18
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Day : Friday
 Date: 11/16/2007

Time: 09:58:04


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = BASSO

First Name = CLAUDE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07440569	5148527	150	11/22/1989	INTERFERENCE FOR INDEPENDENTLY ESTABLISHING A LINK AND TRANSMITTING HIGH LEVEL COMMANDS INCLUDING LOGICAL ADDRESSES FROM DEDICATED MICROPROCESSOR TO SHARED INTELLIGENT MEMORY	BASSO, CLAUDE
07641833	5235700	150	01/16/1991	CHECKPOINTING MECHANISM FOR FAULT-TOLERANT SYSTEMS	BASSO, CLAUDE
07862931	Not Issued	163	04/03/1992	METHOD AND APPARATUS FOR PERFORMING PATTERN SEARCH FUNCTIONS	BASSO, CLAUDE
08018618	Not Issued	166	02/17/1993	HUB AND INTERFACE FOR ISOCRONOUS TOKEN-RING	BASSO, CLAUDE
08120112	5491815	150	09/10/1993	METHOD AND DEVICE FOR CONTROLLING TIMERS ASSOCIATED WITH MULTIPLE USERS IN A DATA PROCESSING SYSTEM	BASSO, CLAUDE
08161354	Not Issued	161	12/02/1993	TOKEN STAR SWITCH	BASSO, CLAUDE
08161372	5444692	250	12/02/1993	TOKEN STAR BRIDGE	BASSO, CLAUDE
08234193	5561807	150	04/28/1994	METHOD AND DEVICE OF MULTICASTING DATA IN A COMMUNICATIONS SYSTEM	BASSO, CLAUDE
08547635	5794033	150	10/24/1995	METHOD AND SYSTEM FOR IN-SITE AND ON-LINE REPROGRAMMING OF HARDWARE LOGICS WITH REMOTE LOADING IN A	BASSO, CLAUDE

NETWORK DEVICE					
<u>08547825</u>	Not Issued	166	10/25/1995	EFFICIENT HARDWARE/SOFTWARE INTERFACE FOR A DATA SWITCH	BASSO, CLAUDE
<u>08548029</u>	5673263	150	10/25/1995	METHOD FOR USING AN IP ADDRESS-BASED ROUTING PROTOCOL IN AN ATM ENVIRONMENT	BASSO, CLAUDE
<u>08554113</u>	5787071	150	11/06/1995	HOP-BY-HOP FLOW CONTROL IN AN ATM NETWORK	BASSO, CLAUDE
<u>08579555</u>	5687356	250	12/27/1995	HUB AND INTERFACE FOR ISOCRONOUS TOKEN RING	BASSO, CLAUDE
<u>08635048</u>	6038592	150	04/19/1996	METHOD AND DEVICE OF MULTICASTING DATA IN A COMMUNICATIONS SYSTEM	BASSO, CLAUDE
<u>08858156</u>	5724348	150	05/05/1997	EFFICIENT HARDWARE/SOFTWARE INTERFACE FOR A DATA SWITCH	BASSO, CLAUDE
<u>08974285</u>	6104715	150	11/19/1997	MERGING OF DATA CELLS IN AN ATM NETWORK	BASSO, CLAUDE
<u>09036354</u>	6370119	150	03/06/1998	COMPUTING THE WIDEST SHORTEST PATH IN HIGH-SPEED NETWORKS	BASSO, CLAUDE
<u>09110917</u>	6195335	150	07/06/1998	DATA SWITCH	BASSO, CLAUDE
<u>09334194</u>	Not Issued	161	06/16/1999	SUPPORT OF NON-HIERARCHICAL PNNI NODES IN A HIERARCHICAL PNNI ATM NETWORK	BASSO, CLAUDE
<u>09334414</u>	7002906	150	06/16/1999	METHOD FOR EXTENDING THE CRANKBACK PROCEDURE TO ANY ASYNCHRONOUS TRANSFER MODE SYSTEM	BASSO, CLAUDE
<u>09334415</u>	7106698	150	06/16/1999	SYSTEM FOR TRIGGERING THE CONTROL PLANE IN AN ASYNCHRONOUS CONNECTION-ORIENTED TRANSMISSION NETWORK	BASSO, CLAUDE
<u>09351712</u>	6633539	150	07/12/1999	DEVICE, METHOD AND ARTICLE OF MANUFACTURE FOR CALL SETUP PACING IN CONNECTION-ORIENTED NETWORKS	BASSO, CLAUDE

<u>09419316</u>	Not Issued	161	10/15/1999	OVERSUBSCRIPTION WITH VIRTUAL PATHS IN ATM NETWORKS	BASSO, CLAUDE
<u>09437820</u>	6690678	150	11/10/1999	METHOD AND SYSTEM IN A PACKET SWITCHING NETWORK FOR DYNAMICALLY ADJUSTING THE BANDWIDTH OF A CONTINUOUS BIT RATE VIRTUAL PATH CONNECTION ACCORDING TO THE NETWORK LOAD	BASSO, CLAUDE
<u>09544312</u>	6658481	150	04/06/2000	A ROUTER USES A SINGLE HIERARCHY INDEPENDENT ROUTING TABLE THAT INCLUDES A FLAG TO LOOK-UP A SERIES OF NEXT HOP ROUTERS FOR ROUTING PACKETS	BASSO, CLAUDE
<u>09544314</u>	6996823	150	04/06/2000	INTER PROCESS COMMUNICATIONS IN A DISTRIBUTED CP AND NP ENVIRONMENT	BASSO, CLAUDE
<u>09544588</u>	6675221	150	04/06/2000	METHOD AND APPARATUS FOR CUSTOMIZING AND FORWARDING PARAMETERS IN A NETWORK PROCESSOR	BASSO, CLAUDE
<u>09546133</u>	6880158	150	04/10/2000	NETWORK PROCESSOR SERVICES ARCHITECTURE THAT IS PLATFORM AND OPERATING SYSTEM INDEPENDENT	BASSO, CLAUDE
<u>09546702</u>	6721800	150	04/10/2000	A SYSTEM USING WEIGHTED NEXT HOP OPTION IN ROUTING TABLE TO INCLUDE PROBABILITY OF ROUTING A PACKET FOR PROVIDING EQUAL COST MULTIPATH FORWARDING PACKETS	BASSO, CLAUDE
<u>09547362</u>	6763375	150	04/11/2000	METHOD FOR DEFINING AND CONTROLLING THE OVERALL BEHAVIOR OF A NETWORK PROCESSOR DEVICE	BASSO, CLAUDE
<u>09621401</u>	6668261	150	07/21/2000	METHOD OF UPGRADING A PROGRAM USING ASSOCIATED CONFIGURATION DATA	BASSO, CLAUDE
<u>09625624</u>	7177951	150	07/26/2000	ADDRESS MANAGEMENT IN PNNI HIERARCHICAL	BASSO, CLAUDE

NETWORKS					
<u>09633767</u>	Not Issued	71	08/07/2000	Alternative routing in PNNI hierarchical networks	BASSO, CLAUDE
<u>09766074</u>	6907007	150	01/19/2001	METHOD OF INJECTING/EXTRACTING CONTROL CELLS IN AN ASYNCHRONOUS TRANSFER MODE (ATM) NETWORK	BASSO, CLAUDE
<u>09828342</u>	7200696	150	04/06/2001	SYSTEM METHOD STRUCTURE IN NEWORK PROCESSOR THAT INDICATES LAST DATA BUFFER OF FRAME PACKET BY LAST FLAG BIT THAT IS EITHER IN FRIST OR SECOND POSITION	BASSO, CLAUDE
<u>09838916</u>	6473434	150	04/20/2001	SCALEABLE AND ROBUST SOLUTION FOR REDUCING COMPLEXITY OF RESOURCE IDENTIFIER DISTRIBUTION IN A LARGE NETWORK PROCESSOR-BASED SYSTEM	BASSO, CLAUDE
<u>09839010</u>	6937606	150	04/20/2001	DATA STRUCTURES FOR EFFICIENT PROCESSING OF IP FRAGMENTATION AND REASSEMBLY	BASSO, CLAUDE
<u>09839079</u>	6836480	150	04/20/2001	DATA STRUCTURES FOR EFFICIENT PROCESSING OF MULTICAST TRANSMISSIONS	BASSO, CLAUDE
<u>09864971</u>	6987735	150	05/24/2001	SYSTEM AND METHOD FOR ENHANCING THE AVAILABILITY OF ROUTING SYSTEMS THROUGH EQUAL COST MULTIPATH	BASSO, CLAUDE
<u>09885782</u>	7003582	150	06/20/2001	ROBUST NP-BASED DATA FORWARDING TECHNIQUES THAT TOLERATE FAILURE OF CONTROL-BASED APPLICATIONS	BASSO, CLAUDE
<u>09898253</u>	7054855	150	07/03/2001	METHOD AND SYSTEM FOR PERFORMING A PATTERN MATCH SEARCH FOR TEXT STRINGS	BASSO, CLAUDE
<u>09931206</u>	7065086	150	08/16/2001	METHOD AND SYSTEM FOR EFFICIENT LAYER 3-LAYER 7 ROUTING OF INTERNET PROTOCOL ("IP") FRAGMENTS	BASSO, CLAUDE

<u>10144075</u>	7280527	150	05/13/2002	LOGICALLY GROUPING PHYSICAL PORTS INTO LOGICAL INTERFACES TO EXPAND BANDWIDTH	BASSO, CLAUDE
<u>10155675</u>	6973503	150	05/23/2002	PREVENTING AT LEAST IN PART CONTROL PROCESSORS FROM BEING OVERLOADED	BASSO, CLAUDE
<u>10174390</u>	7304991	150	06/18/2002	MINIMIZING MEMORY ACCESES FOR A NETWORK IMPLEMENTING DIFFERENTIAL SERVICES OVER MULTI-PROTOCOL LABEL SWITCHING	BASSO, CLAUDE
<u>10191657</u>	7260096	150	07/09/2002	METHOD AND ROUTER FOR FORWARDING INTERNET DATA PACKETS	BASSO, CLAUDE
<u>10217529</u>	7036125	150	08/13/2002	ELIMINATING MEMORY CORRUPTION WHEN PERFORMING TREE FUNCTIONS ON MULTIPLE THREADS	BASSO, CLAUDE
<u>10219643</u>	Not Issued	93	08/15/2002	DATABASE MANAGEMENT SYSTEM AND METHOD OF USING IT TO TRANSMIT PACKETS	BASSO, CLAUDE
<u>10230921</u>	Not Issued	95	08/29/2002	METHOD AND SYSTEM FOR REDUCING LOOK-UP TIME IN PACKET FORWARDING ON COMPUTER NETWORKS	BASSO, CLAUDE
<u>10364069</u>	Not Issued	83	02/11/2003	Frame alteration logic for network processors	BASSO, CLAUDE

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = CALVIGNAC

First Name = JEAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06433609	4493051	150	10/12/1982	COMMUNICATION LINE SCANNING DEVICE FOR A COMMUNICATION CONTROLLER	CALVIGNAC, JEAN
06433744	4491913	150	10/12/1982	ADDRESS GENERATING DEVICE FOR A COMMUNICATION LINE SCANNING DEVICE	CALVIGNAC, JEAN
06892649	4763321	150	08/04/1986	DYNAMIC BANDWIDTH ALLOCATION MECHANISM BETWEEN CIRCUIT SLOTS AND PACKET BIT STREAM IN A COMMUNICATION NETWORK	CALVIGNAC, JEAN
06893073	4761781	150	08/04/1986	ADAPTATIVE PACKET/CIRCUIT SWITCHED TRANSPORTATION METHOD AND SYSTEM	CALVIGNAC, JEAN
07077484	4799219	150	07/24/1987	PSEUDO SYNCHRONOUS TRANSPORT MECHANISM IN A COMMUNICATION NETWORK	CALVIGNAC, JEAN
07077485	4819230	150	07/24/1987	OPTIMIZED BANDWITH ALLOCATION MECHANISM BETWEEN CIRCUIT SLOTS AND PACKET BIT STREAM IN A COMMUNICATION NETWORK	CALVIGNAC, JEAN
07259025	5046069	250	10/17/1988	DATA INTEGRITY SECURING MEANS	CALVIGNAC, JEAN
07266961	4964127	150	11/03/1988	DATA VALIDITY CHECKING MEANS	CALVIGNAC, JEAN
07357225	5119478	150	05/26/1989	PARALLEL PROCESSING METHOD AND DEVICE FOR	CALVIGNAC, JEAN

				RECEIVING AND TRANSMITTING HDLC SDLC BIT STREAMS	
<u>07416165</u>	Not Issued	166	10/02/1989	MECHANISM FOR TRANSFERRING MESSAGES BETWEEN SOURCE AND DESTINATION USERS THROUGH A SHARED MEMORY	CALVIGNAC, JEAN
<u>07479139</u>	Not Issued	166	02/13/1990	SWITCHING SYSTEM FOR SIMULTANEOUSLY TRANSFERRING DATA BETWEEN DATA PROCESSING UNITS	CALVIGNAC, JEAN
<u>07506035</u>	5119376	150	04/06/1990	INTERCONNECTION SYSTEM FOR THE ATTACHMENT OF USER EQUIPMENTS TO A COMMUNICATION PROCESSING UNIT	CALVIGNAC, JEAN
<u>07576876</u>	5197065	150	09/04/1990	DISTRIBUTION MECHANISM FOR ESTABLISHING COMMUNICATIONS BETWEEN USER INTERFACES OF A COMMUNICATION SYSTEM	CALVIGNAC, JEAN
<u>07641833</u>	5235700	150	01/16/1991	CHECKPOINTING MECHANISM FOR FAULT- TOLERANT SYSTEMS	CALVIGNAC, JEAN
<u>07657906</u>	5134636	150	02/20/1991	SYNCHRONIZATION CIRCUIT FOR A SYNCHRONOUS SWITCHING SYSTEM	CALVIGNAC, JEAN
<u>07684111</u>	5251206	150	04/12/1991	HYBRID SWITCHING SYSTEM FOR A COMMUNICATION NODE	CALVIGNAC, JEAN
<u>07740356</u>	Not Issued	163	08/05/1991	MECHANISM FOR PERFORMING THE STEADY STATE AND ERROR RECOVERY FUNCTIONS OF A COMMUNICATION PROTOCOL	CALVIGNAC, JEAN
<u>07830128</u>	5237572	150	01/31/1992	ACTIVE REMOTE MODULE FOR THE ATTACHMENT OF USER EQUIPMENTS TO A COMMUNICATION PROCESSING UNIT	CALVIGNAC, JEAN
<u>07836492</u>	5325404	150	02/18/1992	SYNCHRONIZATION DEVICE FOR PERFORMING	CALVIGNAC, JEAN

				SYNCHRONOUS CIRCUIT SWITCHING FUNCTIONS THRU AN ASYNCHRONOUS COMMUNICATION NODE	
<u>07862931</u>	Not Issued	163	04/03/1992	METHOD AND APPARATUS FOR PERFORMING PATTERN SEARCH FUNCTIONS	CALVIGNAC, JEAN
<u>07903687</u>	5341475	150	06/24/1992	METHOD FOR EXCHANGING MESSAGES BETWEEN A SHARED MEMORY AND COMMUNICATION ADAPTERS USING AN EFFICIENT LOGICAL PROTOCOL	CALVIGNAC, JEAN
<u>07909884</u>	5432910	150	07/07/1992	COUPLING APPARATUS AND METHOD FOR INCREASING THE CONNECTION CAPABILITY OF A COMMUNICATION SYSTEM	CALVIGNAC, JEAN
<u>07965554</u>	5333269	250	10/23/1992	MECHANISM FOR TRANSFERRING MESSAGES BETWEEN SOURCE AND DESTINATION USERS THROUGH A SHARED MEMORY	CALVIGNAC, JEAN
<u>07992314</u>	5572697	250	12/21/1992	APPARATUS FOR RECOVERING LOST BUFFER CONTENTS IN A DATA PROCESSING SYSTEM	CALVIGNAC, JEAN
<u>08005425</u>	5392401	250	01/21/1993	SWITCHING SYSTEM FOR SIMULTANEOUSLY TRANSFERRING DATA BETWEEN DATA PROCESSING UNITS	CALVIGNAC, JEAN
<u>08018618</u>	Not Issued	166	02/17/1993	HUB AND INTERFACE FOR ISOCHRONOUS TOKEN-RING	CALVIGNAC, JEAN
<u>08024061</u>	5467359	150	03/01/1993	APPARATUS FOR GENERATING AND CHECKING THE ERROR CORRECTION CODES OF MESSAGES IN A MESSAGE SWITCHING SYSTEM	CALVIGNAC, JEAN
<u>08036294</u>	5530903	150	03/24/1993	SYSTEM FOR REASSIGNING A HIGHER PRIORITY TO AN INTERRUPTED USER BY INHIBITING THE ACCESS OF OTHER USERS UNTIL THE INTERRUPTED USER HAS	CALVIGNAC, JEAN

COMPLETED ITS TASK					
<u>08043686</u>	Not Issued	161	04/08/1993	HIGH PERFORMANCE DATA RE-ALIGNMENT MECHANISM WITH MULTIPLE BUFFERS IN A MEMORY ACCESS CONTROL DEVICE	CALVIGNAC, JEAN
<u>08120112</u>	<u>5491815</u>	150	09/10/1993	METHOD AND DEVICE FOR CONTROLLING TIMERS ASSOCIATED WITH MULTIPLE USERS IN A DATA PROCESSING SYSTEM	CALVIGNAC, JEAN
<u>08161354</u>	Not Issued	161	12/02/1993	TOKEN STAR SWITCH	CALVIGNAC, JEAN
<u>08161372</u>	<u>5444692</u>	250	12/02/1993	TOKEN STAR BRIDGE	CALVIGNAC, JEAN
<u>08206363</u>	<u>5557266</u>	150	03/07/1994	SYSTEM FOR CASCADING DATA SWITCHES IN A COMMUNICATION NODE	CALVIGNAC, JEAN
<u>08234193</u>	<u>5561807</u>	150	04/28/1994	METHOD AND DEVICE OF MULTICASTING DATA IN A COMMUNICATIONS SYSTEM	CALVIGNAC, JEAN
<u>08446997</u>	<u>5557608</u>	150	05/22/1995	METHOD AND APPARATUS FOR TRANSMISSION OF HIGH PRIORITY TRAFFIC ON LOW SPEED COMMUNICATION LINKS	CALVIGNAC, JEAN
<u>08547635</u>	<u>5794033</u>	150	10/24/1995	METHOD AND SYSTEM FOR IN-SITE AND ON-LINE REPROGRAMMING OF HARDWARE LOGICS WITH REMOTE LOADING IN A NETWORK DEVICE	CALVIGNAC, JEAN
<u>08547825</u>	Not Issued	166	10/25/1995	EFFICIENT HARDWARE/SOFTWARE INTERFACE FOR A DATA SWITCH	CALVIGNAC, JEAN
<u>08547826</u>	<u>5666361</u>	150	10/25/1995	ATM CELL FORWARDING AND LABEL SWAPPING METHOD AND APPARATUS	CALVIGNAC, JEAN
<u>08547827</u>	<u>5668798</u>	150	10/25/1995	MULTIPLEXED TC SUBLAYER FOR ATM SWITCH	CALVIGNAC, JEAN
<u>08547828</u>	<u>5768273</u>	150	10/25/1995	METHOD AND APPARATUS FOR PRIORITY LEVEL QUEUEING IN PROCESSING ATM CELL HEADER AND PAYLOAD	CALVIGNAC, JEAN

<u>08548030</u>	<u>5684797</u>	150	10/25/1995	ATM CELL MULTICASTING METHOD AND APPARATUS	CALVIGNAC, JEAN
<u>08554113</u>	<u>5787071</u>	150	11/06/1995	HOP-BY-HOP FLOW CONTROL IN AN ATM NETWORK	CALVIGNAC, JEAN
<u>08570840</u>	<u>5629928</u>	150	12/12/1995	DYNAMIC FAIR QUEUING TO SUPPORT BEST EFFORT TRAFFIC IN AN ATM NETWORK	CALVIGNAC, JEAN
<u>08579555</u>	<u>5687356</u>	250	12/27/1995	HUB AND INTERFACE FOR ISOCHRONOUS TOKEN RING	CALVIGNAC, JEAN
<u>08635048</u>	<u>6038592</u>	150	04/19/1996	METHOD AND DEVICE OF MULTICASTING DATA IN A COMMUNICATIONS SYSTEM	CALVIGNAC, JEAN
<u>08786914</u>	<u>5946297</u>	150	01/22/1997	N SCHEDULING METHOD AND APPARATUS FOR SUPPORTING ATM CONNECTIONS HAVING A GUARANTEED MINIMUM BANDWIDTH	CALVIGNAC, JEAN
<u>08824941</u>	<u>5923664</u>	150	03/27/1997	METHOD AND APPARATUS PROVIDING A MULTIPORT PHYSICAL INTERFACE TO HIGH SPEED PACKET NETWORKS	CALVIGNAC, JEAN
<u>08858156</u>	<u>5724348</u>	150	05/05/1997	EFFICIENT HARDWARE/SOFTWARE INTERFACE FOR A DATA SWITCH	CALVIGNAC, JEAN
<u>08991911</u>	<u>6144637</u>	150	12/16/1997	DATA COMMUNICATIONS	CALVIGNAC, JEAN
<u>08993695</u>	<u>6003060</u>	250	12/18/1997	METHOD AND APPARATUS TO SHARE RESOURCES WHILE PROCESSING MULTIPLE PRIORITY DATA FLOWS	CALVIGNAC, JEAN

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Inventor Name Search Result

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Last Name = CHANG

First Name = CHIH-JEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09191328	6198535	250	11/13/1998	WAFER ALIGNMENT SYSTEM	CHANG, CHIH-JEN
09193220	6166392	150	11/16/1998	EXPOSURE FOR PERFORMING SYNCHRONIZED OFF-AXIS ALIGNMENT	CHANG, CHIH-JEN
10233989	Not Issued	120	09/02/2002	Method for active queue management with asymmetric congestion control	CHANG, CHIH-JEN
10235672	Not Issued	168	09/05/2002	Receive queue device with efficient queue flow control, segment placement and virtualization mechanisms	CHANG, CHIH-JEN
10235689	Not Issued	133	09/05/2002	Split socket send queue apparatus and method with efficient queue flow control, retransmission and sack support mechanisms	CHANG, CHIH-JEN
10772737	Not Issued	30	02/05/2004	Structure and method for scheduler pipeline design for hierarchical link sharing	CHANG, CHIH-JEN
10868725	Not Issued	41	06/15/2004	Method and structure for enqueueing data packets for processing	CHANG, CHIH-JEN
10899937	7277982	150	07/27/2004	DRAM ACCESS COMMAND QUEUING STRUCTURE	CHANG, CHIH-JEN
11030344	Not Issued	30	01/06/2005	Apparatus and method for efficiently modifying network data frames	CHANG, CHIH-JEN
11042692	Not Issued	83	01/25/2005	Systems and methods for TDM multithreading	CHANG, CHIH-JEN
11062036	Not Issued	30	02/18/2005	Method for sharing single data buffer by several packets	CHANG, CHIH-JEN
11070060	7293158	150	03/02/2005	SYSTEMS AND METHODS	CHANG, CHIH-JEN

				FOR IMPLEMENTING COUNTERS IN A NETWORK PROCESSOR WITH COST EFFECTIVE MEMORY	
<u>11070758</u>	Not Issued	41	03/02/2005	Systems and methods for adaptively mapping an instruction cache	CHANG, CHIH-JEN
<u>11076218</u>	Not Issued	41	03/09/2005	Systems and methods for multi-frame control blocks	CHANG, CHIH-JEN
<u>11091245</u>	Not Issued	30	03/28/2005	Flexible control block format for frame description and management	CHANG, CHIH-JEN
<u>11096353</u>	Not Issued	30	04/01/2005	System and method for a method for reducing latency in a host ethernet adapter (HEA)	CHANG, CHIH-JEN
<u>11096362</u>	Not Issued	30	04/01/2005	Method and system for performing a packet header lookup	CHANG, CHIH-JEN
<u>11096363</u>	Not Issued	30	04/01/2005	Method and system for accommodating several ethernet ports and a wrap transmitted flow handled by a simplified frame-by-frame upper structure	CHANG, CHIH-JEN
<u>11096365</u>	Not Issued	30	04/01/2005	System and method for parsing, filtering, and computing the checksum in a host ethernet adapter (HEA)	CHANG, CHIH-JEN
<u>11096571</u>	Not Issued	30	04/01/2005	Method and apparatus for providing a network connection table	CHANG, CHIH-JEN
<u>11097051</u>	Not Issued	30	04/01/2005	Network communications for operating system partitions	CHANG, CHIH-JEN
<u>11097055</u>	Not Issued	30	04/01/2005	Method and apparatus for blind checksum and correction for network transmissions	CHANG, CHIH-JEN
<u>11097430</u>	Not Issued	30	04/01/2005	System and method for computing a blind checksum in a host ethernet adapter (HEA)	CHANG, CHIH-JEN
<u>11097608</u>	Not Issued	30	04/01/2005	Host ethernet adapter for networking offload in server environment	CHANG, CHIH-JEN
<u>11097652</u>	Not Issued	41	04/01/2005	Configurable ports for a host Ethernet adapter	CHANG, CHIH-JEN
<u>11108485</u>	Not Issued	30	04/18/2005	Systems and methods for weighted best effort scheduling	CHANG, CHIH-JEN
<u>11119329</u>	Not	30	04/29/2005	Systems and methods for rate-	CHANG, CHIH-JEN

	Issued			limited weighted best effort scheduling	
<u>11133477</u>	Not Issued	30	05/18/2005	Method and system for flexible network processor scheduler and data flow	CHANG, CHIH-JEN
<u>11418606</u>	Not Issued	30	05/05/2006	Split socket send queue apparatus and method with efficient queue flow control, retransmission and sack support mechanisms	CHANG, CHIH-JEN
<u>11422087</u>	Not Issued	25	06/05/2006	MULTIPLE-LEVEL DATA PROCESSING SYSTEM	CHANG, CHIH-JEN
<u>11487265</u>	Not Issued	30	07/14/2006	Receive queue device with efficient queue flow control, segment placement and virtualization mechanisms	CHANG, CHIH-JEN
<u>11669419</u>	Not Issued	30	01/31/2007	System and Method for Multicore Communication Processing	CHANG, CHIH-JEN
<u>11832220</u>	Not Issued	25	08/01/2007	DRAM ACCESS COMMAND QUEUING METHOD	CHANG, CHIH-JEN

Inventor Search Completed: No Records to Display.

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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = DAVIS

First Name = GORDON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09775443	6435419	150	02/02/2001	LIQUID AIR FRESHENER DISPENSING DEVICE FOR A DUCT	DAVIS, GORDON D.
07609903	Not Issued	161	11/05/1990	SYSTEM FOR LASER TREATMENT OF THE FEMALE URETHRA AND BLADDER	DAVIS, GORDON D.
07879954	5342353	150	05/06/1992	SYSTEM FOR LASER TREATMENT OF THE FEMALE URETHRA AND BLADDER	DAVIS, GORDON D.
08297582	Not Issued	161	08/29/1994	SYSTEM FOR LASER TREATMENT OF THE FEMALE URETHRA AND BLADDER	DAVIS, GORDON D.
07231102	Not Issued	168	08/11/1988	APPARATUS FOR PACKAGING ELECTRONIC CIRCUITS	DAVIS, GORDON H.
07506510	Not Issued	161	04/09/1990	APPARATUS FOR PACKAGING ELECTRONIC CIRCUITS	DAVIS, GORDON H.
06216450	4359146	150	12/15/1980	ROTATION SENSING CONTROL	DAVIS, GORDON J.
07746558	D344411	150	08/19/1991	WRIST LIGHT MOUNT	DAVIS, GORDON P.
08217252	Not Issued	161	03/23/1994	METHOD AND APPARATUS FOR EFFICIENTLY CONNECTING A NETWORK TO A FILE SERVER	DAVIS, GORDON R.
10370550	7286543	150	02/20/2003	MEMORY SYSTEM WITH APPARATUS AND METHOD TO ENABLE BALANCED BANDWIDTH UTILIZATION	DAVIS, GORDON T.
10448528	6963868	150	05/30/2003	MULTI-BIT PATRICIA TREES	DAVIS, GORDON T.
10453245	7149749	150	06/03/2003	METHOD OF INSERTING AND	DAVIS, GORDON

				DELETING LEAVES IN TREE TABLE STRUCTURES	T.
<u>10654139</u>	Not Issued	89	09/03/2003	Data structure supporting random delete and aging/timer function	DAVIS, GORDON T.
<u>10654502</u>	Not Issued	71	09/03/2003	Data structure supporting session timer and variable aging function including self adjustable 2MSL	DAVIS, GORDON T.
<u>10662007</u>	Not Issued	71	09/11/2003	Apparatus and method for caching lookups based upon TCP traffic flow characteristics	DAVIS, GORDON T.
<u>10772737</u>	Not Issued	30	02/05/2004	Structure and method for scheduler pipeline design for hierarchical link sharing	DAVIS, GORDON T.
<u>10782617</u>	Not Issued	71	02/19/2004	Active flow management with hysteresis	DAVIS, GORDON T.
<u>10832634</u>	Not Issued	93	04/27/2004	METHOD AND SYSTEM FOR COMPRESSING A SEARCH TREE STRUCTURE USED IN RULE CLASSIFICATION	DAVIS, GORDON T.
<u>10832957</u>	Not Issued	120	04/27/2004	Method and system for compressing multi-field rule specifications	DAVIS, GORDON T.
<u>10832958</u>	Not Issued	41	04/27/2004	Method and system for managing multi-field classification rules relating to ingress contexts and egress contexts	DAVIS, GORDON T.
<u>10868725</u>	Not Issued	41	06/15/2004	Method and structure for enqueueing data packets for processing	DAVIS, GORDON T.
<u>10879964</u>	7089240	150	06/29/2004	LONGEST PREFIX MATCH LOOKUP USING HASH FUNCTION	DAVIS, GORDON T.
<u>10894628</u>	Not Issued	30	07/20/2004	Multi-field classification dynamic rule updates	DAVIS, GORDON T.
<u>10899937</u>	7277982	150	07/27/2004	DRAM ACCESS COMMAND QUEUING STRUCTURE	DAVIS, GORDON T.
<u>11094399</u>	Not Issued	71	03/30/2005	Performance of a cache by detecting cache lines that have been reused	DAVIS, GORDON T.
<u>11132656</u>	Not Issued	41	05/19/2005	Methods and apparatus for sharing processor resources	DAVIS, GORDON T.
<u>11132658</u>	Not Issued	41	05/19/2005	Methods and apparatus for dynamically switching processor mode	DAVIS, GORDON T.
<u>11348417</u>	Not	25	02/07/2006	Network congestion detection and	DAVIS, GORDON

	Issued			automatic fallback: methods, systems & program products	T.
<u>11353841</u>	Not Issued	30	02/14/2006	Longest prefix match lookup using hash function	DAVIS, GORDON T.
<u>11462404</u>	Not Issued	30	08/04/2006	METHOD AND STRUCTURE FOR DELETING LEAVES IN TREE TABLE STRUCTURES	DAVIS, GORDON T.
<u>11534711</u>	Not Issued	30	09/25/2006	Method and Apparatus for Register Renaming in a Microprocessor	DAVIS, GORDON T.
<u>11538445</u>	Not Issued	30	10/04/2006	Apparatus and Method for Supporting Simultaneous Storage of Trace and Standard Cache Lines	DAVIS, GORDON T.
<u>11538458</u>	Not Issued	30	10/04/2006	Apparatus and Method for Saving Power in a Trace Cache	DAVIS, GORDON T.
<u>11538831</u>	Not Issued	30	10/05/2006	Apparatus and Method for Using Branch Prediction Heuristics for Determination of Trace Formation Readiness	DAVIS, GORDON T.
<u>11559512</u>	Not Issued	30	11/14/2006	Apparatus and Method for Cache Maintenance	DAVIS, GORDON T.
<u>11561908</u>	Not Issued	30	11/21/2006	Instruction Cache Trace Formation	DAVIS, GORDON T.
<u>11776162</u>	Not Issued	19	01/01/0001	MEMORY SYSTEM WITH APPARATUS AND METHOD TO ENABLE BALANCED BANDWIDTH UTILIZATION	DAVIS, GORDON T.
<u>11832220</u>	Not Issued	25	08/01/2007	DRAM ACCESS COMMAND QUEUING METHOD	DAVIS, GORDON T.
<u>60301598</u>	Not Issued	159	06/27/2001	Ordered semaphore management using the completion unit	DAVIS, GORDON T.
<u>60301602</u>	Not Issued	159	06/27/2001	Network processor with semaphore subsystem	DAVIS, GORDON T.
<u>06106824</u>	4357678	150	12/26/1979	PROGRAMMABLE SEQUENTIAL LOGIC ARRAY MECHANISM	DAVIS, GORDON T.
<u>06194724</u>	4362486	150	10/07/1980	AUTOMATIC MULTILAYER CERAMIC (MLC) SCREENING MACHINE	DAVIS, GORDON T.
<u>06194727</u>	4304536	150	10/07/1980	GREEN SHEET SUPPORT FIXTURE SPEED AND POSITION CONTROL SYSTEM FOR A SCREENING MACHINE	DAVIS, GORDON T.

<u>06876625</u>	<u>5010476</u>	150	06/20/1986	TIME MULTIPLEXED SYSTEM FOR TIGHTLY COUPLING PIPELINED PROCESSORS TO SEPARATE SHARED INSTRUCTION AND DATA STORAGE UNITS	DAVIS, GORDON T.
<u>07214250</u>	<u>4849706</u>	150	07/01/1988	DIFFERENTIAL PHASE MODULATION DEMODULATOR	DAVIS, GORDON T.
<u>07227582</u>	<u>4899366</u>	150	08/02/1988	TAP ROTATION N FRACTIONALLY SPACED EQUALIZER TO COMPENSATE FOR DRIFT DUE TO FIXED SAMPLE RATE	DAVIS, GORDON T.
<u>07227832</u>	<u>4991169</u>	150	08/02/1988	REAL-TIME DIGITAL SIGNAL PROCESSING RELATIVE TO MULTIPLE DIGITAL COMMUNICATION CHANNELS	DAVIS, GORDON T.
<u>07254985</u>	<u>4972342</u>	150	10/07/1988	PROGRAMMABLE PRIORITY BRANCH CIRCUIT	DAVIS, GORDON T.
<u>07254986</u>	<u>4991133</u>	150	10/07/1988	SPECIALIZED COMMUNICATIONS PROCESSOR FOR LAYERED PROTOCOLS	DAVIS, GORDON T.
<u>07289064</u>	<u>4871974</u>	150	12/23/1988	COHERENT PHASE SHIFT KEYED DEMODULATOR	DAVIS, GORDON T.

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Your Search was:

Last Name = VERPLANKEN

First Name = FABRICE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07684111	5251206	150	04/12/1991	HYBRID SWITCHING SYSTEM FOR A COMMUNICATION NODE	VERPLANKEN, FABRICE
08018618	Not Issued	166	02/17/1993	HUB AND INTERFACE FOR ISOCHRONOUS TOKEN-RING	VERPLANKEN, FABRICE
08024061	5467359	150	03/01/1993	APPARATUS FOR GENERATING AND CHECKING THE ERROR CORRECTION CODES OF MESSAGES IN A MESSAGE SWITCHING SYSTEM	VERPLANKEN, FABRICE
08161354	Not Issued	161	12/02/1993	TOKEN STAR SWITCH	VERPLANKEN, FABRICE
08161372	5444692	250	12/02/1993	TOKEN STAR BRIDGE	VERPLANKEN, FABRICE
08206363	5557266	150	03/07/1994	SYSTEM FOR CASCADING DATA SWITCHES IN A COMMUNICATION NODE	VERPLANKEN, FABRICE
08234193	5561807	150	04/28/1994	METHOD AND DEVICE OF MULTICASTING DATA IN A COMMUNICATIONS SYSTEM	VERPLANKEN, FABRICE
08547635	5794033	150	10/24/1995	METHOD AND SYSTEM FOR IN-SITE AND ON-LINE REPROGRAMMING OF HARDWARE LOGICS WITH REMOTE LOADING IN A NETWORK DEVICE	VERPLANKEN, FABRICE
08547825	Not Issued	166	10/25/1995	EFFICIENT HARDWARE/SOFTWARE INTERFACE FOR A DATA SWITCH	VERPLANKEN, FABRICE

08547826	5666361	150	10/25/1995	ATM CELL FORWARDING AND LABEL SWAPPING METHOD AND APPARATUS	VERPLANKEN, FABRICE
08547827	5668798	150	10/25/1995	MULTIPLEXED TC SUBLAYER FOR ATM SWITCH	VERPLANKEN, FABRICE
08547828	5768273	150	10/25/1995	METHOD AND APPARATUS FOR PRIORITY LEVEL QUEUEING IN PROCESSING ATM CELL HEADER AND PAYLOAD	VERPLANKEN, FABRICE
08548030	5684797	150	10/25/1995	ATM CELL MULTICASTING METHOD AND APPARATUS	VERPLANKEN, FABRICE
08554113	5787071	150	11/06/1995	HOP-BY-HOP FLOW CONTROL IN AN ATM NETWORK	VERPLANKEN, FABRICE
08570840	5629928	150	12/12/1995	DYNAMIC FAIR QUEUING TO SUPPORT BEST EFFORT TRAFFIC IN AN ATM NETWORK	VERPLANKEN, FABRICE
08579555	5687356	250	12/27/1995	HUB AND INTERFACE FOR ISOCRONOUS TOKEN RING	VERPLANKEN, FABRICE
08635048	6038592	150	04/19/1996	METHOD AND DEVICE OF MULTICASTING DATA IN A COMMUNICATIONS SYSTEM	VERPLANKEN, FABRICE
08754562	5912881	150	11/21/1996	METHOD AND APPARATUS FOR FAST CHECKING THE FRAME CHECK SEQUENCE OF A SEGMENTED MESSAGE	VERPLANKEN, FABRICE
08824941	5923664	150	03/27/1997	METHOD AND APPARATUS PROVIDING A MULTIPORT PHYSICAL INTERFACE TO HIGH SPEED PACKET NETWORKS	VERPLANKEN, FABRICE
08858156	5724348	150	05/05/1997	EFFICIENT HARDWARE/SOFTWARE INTERFACE FOR A DATA SWITCH	VERPLANKEN, FABRICE
08943606	6044079	150	10/03/1997	STATISTICAL PACKET DISCARD	VERPLANKEN, FABRICE
08991911	6144637	150	12/16/1997	DATA COMMUNICATIONS	VERPLANKEN, FABRICE
08993695	6003060	250	12/18/1997	METHOD AND APPARATUS	VERPLANKEN,

				TO SHARE RESOURCES WHILE PROCESSING MULTIPLE PRIORITY DATA FLOWS	FABRICE
<u>09110917</u>	6195335	150	07/06/1998	DATA SWITCH	VERPLANKEN, FABRICE
<u>09110920</u>	6370148	150	07/06/1998	DATA COMMUNICATIONS	VERPLANKEN, FABRICE
<u>09477578</u>	6539394	150	01/04/2000	METHOD AND SYSTEM FOR PERFORMING INTERVAL-BASED TESTING OF FILTER RULES	VERPLANKEN, FABRICE
<u>09706969</u>	7061860	150	11/06/2000	NETWORK TRAFFIC SHAPING	VERPLANKEN, FABRICE
<u>10248010</u>	7103822	150	12/10/2002	METHOD AND APPARATUS FOR COMPUTING 'N-BIT AT A TIME' CRC'S OF DATA FRAMES OF LENGTHS NOT MULTIPLE OF N	VERPLANKEN, FABRICE
<u>10328355</u>	6789234	150	12/23/2002	METHOD AND SYSTEM FOR A TIMING BASED LOGIC ENTRY	VERPLANKEN, FABRICE
<u>09546651</u>	6657962	150	04/10/2000	METHOD AND SYSTEM FOR MANAGING CONGESTION IN A NETWORK	VERPLANKEN, FABRICE J.
<u>09548910</u>	6862292	150	04/13/2000	METHOD AND SYSTEM FOR NETWORK PROCESSOR SCHEDULING OUTPUTS BASED ON MULTIPLE CALENDARS	VERPLANKEN, FABRICE J.
<u>10068392</u>	6757795	150	02/05/2002	APPARATUS AND METHOD FOR EFFICIENTLY SHARING MEMORY BANDWIDTH IN A NETWORK PROCESSOR	VERPLANKEN, FABRICE J.
<u>10301247</u>	Not Issued	93	11/21/2002	APPARATUS, METHOD AND PROGRAM PRODUCT TO GENERATE AND USE CRC IN COMMUNICATIONS NETWORK	VERPLANKEN, FABRICE J.
<u>10772737</u>	Not Issued	30	02/05/2004	Structure and method for scheduler pipeline design for hierarchical link sharing	VERPLANKEN, FABRICE J.
<u>10850296</u>	Not Issued	93	05/20/2004	3	VERPLANKEN, FABRICE J.
<u>10868725</u>	Not Issued	41	06/15/2004	Method and structure for enqueueing data packets for	VERPLANKEN, FABRICE J.

				processing	
<u>10899937</u>	7277982	150	07/27/2004	DRAM ACCESS COMMAND QUEUING STRUCTURE	VERPLANKEN, FABRICE J.
<u>11030344</u>	Not Issued	30	01/06/2005	Apparatus and method for efficiently modifying network data frames	VERPLANKEN, FABRICE J.
<u>11062036</u>	Not Issued	30	02/18/2005	Method for sharing single data buffer by several packets	VERPLANKEN, FABRICE J.
<u>11091245</u>	Not Issued	30	03/28/2005	Flexible control block format for frame description and management	VERPLANKEN, FABRICE J.
<u>11133477</u>	Not Issued	30	05/18/2005	Method and system for flexible network processor scheduler and data flow	VERPLANKEN, FABRICE J.
<u>11142937</u>	Not Issued	30	06/02/2005	Alternate representation of integers for efficient implementation of addition of a sequence of multiprecision integers	VERPLANKEN, FABRICE J.
<u>11669419</u>	Not Issued	30	01/31/2007	System and Method for Multicore Communication Processing	VERPLANKEN, FABRICE J.
<u>11832220</u>	Not Issued	25	08/01/2007	DRAM ACCESS COMMAND QUEUING METHOD	VERPLANKEN, FABRICE J.
<u>11875995</u>	Not Issued	19	10/22/2007	APPARATUS, METHOD AND PROGRAM PRODUCT TO GENERATE AND USE CRC IN COMMUNICATIONS NETWORK	VERPLANKEN, FABRICE J.
<u>60196831</u>	Not Issued	159	04/13/2000	Method and system for network processor based on service levels	VERPLANKEN, FABRICE J.
<u>60276739</u>	Not Issued	159	03/16/2001	3b/4b Line coding for 2-bit channels	VERPLANKEN, FABRICE J.
<u>60281063</u>	Not Issued	159	04/03/2001	Apparatus and method for efficiently sharing memory bandwidth in a network processor	VERPLANKEN, FABRICE J.
<u>08974285</u>	6104715	150	11/19/1997	MERGING OF DATA CELLS IN AN ATM NETWORK	VERPLANKEN, FABRICE J.
<u>09477179</u>	6557053	150	01/04/2000	QUEUE MANAGER FOR A BUFFER	VERPLANKEN, FABRICE JEAN

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